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Step-1: Add element-1 of impedance 0.25 pu from the external node-1 (q=1) to internal ref. node-0 (p=0). (Case-a), as shown in the partial network; Step-2: Add element-2 of impedance 0.2 pu from the external node-3 (q=3) to internal ref. node-0 (p=0). (Case-a), as shown in the partial network;

COMPUTER TECHNIQUES IN POWER SYSTEMS (CTPS)

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