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The annual Conference on Cryptographic Hardware and Embedded Systems (CHES) is the premier venue for research on design and analysis of cryptographic hardware and software implementations. As an area conference of the International Association for Cryptologic Research (IACR), CHES bridges the cryptographic research and engineering communities, and attracts participants from academia, industry, government and beyond.

CHES

This book constitutes the refereed proceedings of the 17th International Workshop on Cryptographic Hardware and Embedded Systems, CHES 2015, held in Saint Malo, France, in September 2015. The 34 full papers included in this volume were carefully reviewed and selected from 128 submissions. They are organized

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in the following topical sections: processing techniques in side-channel analysis; cryptographic hardware implementations; homomorphic encryption in hardware; side-channel attacks on ...

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Cryptographic Hardware and Embedded Systems (CHES) is a conference for cryptography research, focusing on the implementation of cryptographic algorithm. The two general areas treated are the efficient and the secure implementation of algorithms.

~~International Association for Cryptologic Research — Wikipedia~~

Modern cryptographic algorithms can be implemented using dedicated cryptographic hardware or software running on general-purpose hardware. For various reasons, dedicated cryptographic hardware...

~~Cryptographic Implementations: Hardware vs. Software ...~~

Since 1999, the annual CHES conference highlights new results in the design and analysis of cryptographic hardware and software implementations and builds a valuable bridge between the research and cryptographic engineering communities and attracts participants from industry, academia, and government organizations. Topics suitable for CHES include, but are not limited to cryptographic implementations, attacks against implementations and countermeasures,

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Of special interest are contributions that describe new methods for secure and efficient hardware implementations, and high-speed or leak-resistant software for embedded systems, e.g. smart cards, microprocessors, DSPs, etc. The workshop aims to bridge the gap between the cryptography research community and the application areas of cryptography.

~~Workshop on Cryptographic Hardware and Embedded Systems ...~~

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IACR Transactions on Cryptographic Hardware and Embedded Systems (TCHES), Volume 2020, Issue 1 Submission: 15 July 2019 Rebuttal: 22 – 26 August 2019 Notification: 15 September 2019 Camera-ready: 14 October 2019 IACR Transactions on Cryptographic Hardware and Embedded Systems (TCHES), Volume 2020, Issue 2 Submission: 15 October 2019

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Workshop on Cryptographic Hardware and Embedded Systems. Workshop on Cryptographic Hardware and Embedded Systems is a workshop for cryptography research, focusing on the implementation of cryptographic algorithm. The two general areas treated are the efficient and the secure implementation of algorithms. Related topics such as random number generators or special-purpose cryptanalytical machines are also commonly covered at the workshop.

~~What does workshop on cryptographic hardware and embedded ...~~

The need for fast but secure cryptographic systems is growing bigger. Therefore, dedicated hardware for cryptography is becoming a key issue for designers. With the spread of reconfigurable hardware such as FPGAs, embedded cryptographic hardware became cost-effective.

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