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known as complementary-symmetry
metal-oxide-semiconductor, is a type of
metal-oxide-semiconductor field-effect
transistor fabrication process that uses
complementary and symmetrical pairs of p-type
and n-type MOSFETs for logic functions. CMOS
technology is used for constructing
integrated circuit chips, including
microprocessors, microcontrollers, memory
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Ø Largest fraction of digital IC market until 80 ' s. Ø However, power consumption per gate sets upper limit on the integration density.

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- MOS (Metal-Oxide-Silicon) Ø First CMOS introduced in 1963. Ø PMOS-only used through early 70's (in the first practical MOS integrated circuits, e.g., calculators).

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