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~~According to the advertising copy, one of these two chips is a custom ASIC ... low 24 MHz sampling rate. That gave me a good overview of the signals and confirmed that the device goes into a low ...~~

~~What's Inside A Neocode Laser Sensor?~~

~~Parallax didn't just jump into some cookie cutter ASIC, they made their own parallel multi-core microcontroller. Designed by [Chip Gracey], the Parallax Propeller has 8 cores, called cogs.~~

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Hacklet 73 – Parallax Propeller Projects

This paper presents a convenient method for a Java processor to work with dynamic instruction set in the form of FPGA or ASIC. The costs of area, power and timing are trivial. Such idea is also ...

Dynamic instruction set load-in method for Java SoC

Considering the 10mm of extra foam that Asics have added to the shoe, they still offer good stability, minimal weight (263g) and we didn't feel like we were teetering around when we were out ...

12 best men's running shoes to help you smash your personal best

The D-flex groove in the midfoot is designed to help to you turn and accelerate with power, and the shoe certainly ... of that), with exceptional comfort. Asics designs its tennis shoes according ...

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In extreme cases the entire SoC can be put in power-down mode except for its real-time clock, but the time taken to wake up from low-power mode can be an issue. It is essential to integrate a ...

Using ARM Processor-based Flash MCUs as a Platform for Custom Systems-on-Chip
I thought his ASIC was probably on the high side, I expect, for the longer life development (but okay for a stoping operation) .. bulk undergone should be lower - \$500-800 maybe as low \$400 if the ...

Greatland Gold Share Chat

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