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~~Figure 2 shows a block diagram of the CD4046B, which has been implemented on a single monolithic integrated circuit. The PLL structure consists of a low-power, linear VCO and two different phase comparators, having a common signal-input amplifier and a common comparator input. A 5.2-V Zener diode is provided for supply regulation, if necessary.~~

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The HC/HCT4046A PLL with VCO is a high-speed CMOS IC designed for use in general-purpose PLL applications, including frequency modulation, demodulation, discrimination, synthesis, and multiplication.

~~CMOS Phase-Locked Loop Applications (Rev. B)~~

Phase-Locked Loop Design Fundamentals Application Note, Rev. 1.0 2 Freescale Semiconductor with the reader. Since the scope of this article is practical in nature all theoretical derivations have been omitted, hoping to simplify and clarify the content. A bibliography is included for those who desire to pursue the theoretical aspect.

~~Phase-Locked Loop Design Fundamentals~~

The 4046 chip is a phase-locked loop. A phase locked loop IC consists of a voltage-controlled oscillator (VCO) and a phase detector. In this circuit, we will use only the VCO portion of the 4046 IC and not the phase detector. We use the chip just to generate a digital signal that can be used a clock signal.

~~How to Build a Voltage-controlled Oscillator (VCO) Circuit ...~~

The IC 4046 is a Phase-locked loop IC of CMOS digital (combined analog and digital chip). A Phase-locked loop (PLL) has a voltage-controlled oscillator (VCO). The VCO signal and an input signal are sent to a phase comparator which generates an error voltage part to any difference in frequency between two signals.

~~CD4046 circuit datasheet (phase locked loop) | ElecCircuit.com~~

Figure 2 - functional diagram of the 4046 phase-locked-loop with vco The exact ranges and component values are determined by extensive charts included in the 4046 data sheet - (443K) in PDF format. This gives us a very flexible VCO capable of operating anywhere up to 17 Mhz, something the early CMOS versions were incapable of doing.

~~74HC4046 phase-locked loop - electronics tutorials~~

CD4046 Ten Times 10× Frequency Multiplier Circuit. For 1Hz to 1KHz input range, we design a VCO to cover 10Hz to 10KHz, with some extra range on each end. the VCO output is divided by 10 and then compared to the input signal using the wideband phase detector. The phase-detector output goes to the loop filter and then reaches around and closes the loop by running the VCO.

~~4046 Ten Times Frequency Multiplier Circuit | Electronic ...~~

There are many applications of a PLL circuit. PLL circuits can demodulate an FM frequency, multiply frequencies, synchronize processor clocks, and a host of other things. The inside of a 4046 IC...

~~Phase-Locked Loop: FM Demodulator - ELEC101~~

The Design of Phase-Locked-Loop Circuit for Precision Capacitance ... J., "Low power consumption phaselocked loop 4046 applied research and simulation," Popular science technology, Vol. 16, Issue ...

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